COPY OF PAPERS ORIGINALLY FILED

PTO/SB/96 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the apperwork Reduction Action 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

STATEMENT UNDE	R 37 CFR 3.73(b)	
Applicant/Patent Owner: Quicktum Design Systems, Inc.		
Application No./Patent No.: 09/655,596 File	ed/issue Date: 09-06-00	
Entitled: <u>High Speed Software Driven Emulator Comprised of a</u> Memory Read/Writes Without Interrupting the Emulation.	a Plurality of Emulation Processors with a Method to Allow	
Quicktum Design Systems, Inc a corporation		
	nee, e.g., corporation, partnership, university, governmental (CO)	VED
states that it is:		
1. the assignee of the entire right, title, and interest; of	Technology Ce	2002
2. an assignee of an undivided part interest	Technology Co	ntor 0400
in the patent application/patent identified above by virtue of a chapplication/patent identified above, to the current assign	nam of the nom me inventor(3), of the patent	nter 2100
From: William F. Beausoleil, Bryan R. Cook, Tak-K Thomas and Norton J. Tomassetti	(wong Ng, Helmut Roth, Peter Tannenbaum, Lawrence A.	
To: International Business Machines Corporation The document was recorded in the United States For for which a copy thereof is attached.	Patent and Trademark Office at Reel <u>011145,</u> Frame <u>0432,</u>	1
From: International Business Machines Corporation T avidencing this assignment is being recorded herewith.	o: Cadence Design Systems, Inc. A copy of a document A copy of this document is attached as Exhibit 1.	
3. From: Cadence Design Systems, Inc. To: Quickturn I this assignment is being recorded herewith. A copy of the co	Design Systems, Inc. A copy of a document evidencing his document is attached as Exhibit 2.	
[NOTE: A separate copy (i.e., the original assignment docum must be submitted to Assignment Division in accordance with recorded in the records of the USPTO. See MPEP 302.8]		
The undersigned (whose title is supplied below) is empowered to	o sign this statement on behalf of the assignee.	
April 22, 2002	Pupu Kerft	
Date	Signature	
	R.L. Smith McKeithen	
	Typed or printed name	
	Secretary	
	Title	

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

PTO/SB/82 (10-00)

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REVOCATION OF POWER OF ATTORNEY OR AUTHORIZATION OF AGENT

09/655,596
09-06-00
Beausoleil, W.F., et al
2123
Not Yet Assigned
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Name	R. L. Smith McKeithen, Secretary								
Signature	Signature M Melleth								
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NOTE: Signatu	res of all the inv	ventors or assign	ees of record of the is required, see	ne entire ir	nterest or their r	epresen	tative(s) are r	equired.	
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NOTICE OF PATENT ASSIGNMENT

COPY OF PAPERS ORIGINALLY FILED

This NOTICE OF PATENT ASSIGNMENT (the "Notice") is executed as of the date below, by INTERNATIONAL BUSINESS MACHINES CORPORATION, a New York corporation ("IBM").

WHEREAS, IBM has entered into a "Patent Assignment Agreement" (the "Agreement") with CADENCE DESIGN SYSTEMS, INC., a Delaware corporation ("CADENCE"), dated February 27, 2002 ("Effective Date"), pursuant to which IBM has agreed to, among other things, assign to CADENCE certain United States patents.

WHEREAS, IBM and CADENCE wish to evidence the assignment of such patent rights made in the Agreement with this document.

NOW, THEREFORE, for good and valuable consideration, IBM agrees as follows:

ARTICLE I IBM PATENTS

RECEIVED

"IBM Patents" shall mean the patent applications listed in Attachment AMAY 0 9 2002

ARTICLE II ASSIGNMENTS

Technology Center 2100

Effective on the Effective Date, and subject to the terms and conditions of the Agreement, including certain reserved rights, IBM has assigned, transferred and conveyed to CADENCE all of IBM's right, title and interest in the IBM Patents.

IN WITNESS WHEREOF, IBM has caused this Notice to be executed.

INTERNATIONAL BUSINESS MACHINES CORPORATION

Gerald Rosenthal

Vice President, Intellectual Property and Licensing

Date: March 8, 2002

ATTACHMENT A TO IBM PATENT ASSIGNMENT ACKNOWLEDGEMENT

09/655,596

09/656,541

09/193,733

09/523,053

09/656,146

09/373,125

09/655,595

09/656,147

09/796,055

09/522,354

PTO/SB/96 (08-00)
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	STATEMENT UNDER 37 CFR 3.73(b)	3
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S	Application No./Patent No.: 09/655,596 Filed/Issue Date: 09-06-00	ORIGINALLY FILED
/	Entitled: High Speed Software Driven Emulator Comprised of a Plurality of Emulation Processors with a Method to Allow	1 3
	Memory Read/Writes Without Interrupting the Emulation.	1 / 1/2
_	Cadence Design Systems, Inc , a corporation	\ \ \ \
	(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)	
	states that it is: 1. the assignee of the entire right, title, and interest; or 2. an assignee of an undivided part interest name of the entire right, title, and interest; or 2. name of the entire right, title, and interest; or	FD
	1. X the assignee of the entire right, title, and interest; or	
	2. an assignee of an undivided part interest MAY 0 9 20	02
	in the patent application/patent identified above to the current assignee as shown below:	0400
	application/patent identified above, to the current assignee as shown below:	er 2100
	 From: William F. Beausoleil, Bryan R. Cook, Tak-Kwong Ng, Helmut Roth, Peter Tannenbaum, Lawrence A. Thomas and Norton J. Tomassetti 	
	Todata and Foreign and Machines Companying	
	To: International Business Machines Corporation The document was recorded in the United States Patent and Trademark Office at Reel 011145, Frame 0432,	
	or for which a copy thereof is attached.	
	From: International Business Machines Corporation To: Cadence Design Systems, Inc. A copy of a document evidencing this assignment is being recorded herewith. A copy of this document is attached as Exhibit 1.	
	[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.8]	
•	The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.	
	April 22, 2002 Par-Kif	
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	R.L. Smith McKeithen	
	Typed or printed name	
	Secretary	
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ny ra	rden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. y comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and demark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant mmissioner for Patents, Washington, DC 20231.	



PTO/SB/81 (02-01)
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Application Number	09/655,596	
Filing Dat	09-06-00	
First Named Inv ntor	Beausoleil, W.F., et al	
Group Art Unit	2123	
Examiner Name	Not Yet Assigned	
Attorney Docket Number	272/206	

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· · ·		Sid	GNATURE of Applic	ant or As	signee of Record			
Name	R.L. Sm	ith McKeithen, Se	cretary, Quickturn De	esign Syste	ems, Inc.			
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Burden Hour Statement: This form is estimated to take 3 minutes to complete. Time will vary depending upon the needs of the individual case. Any Comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

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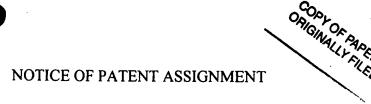
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Approved for use through 10/31/2002. OMB 0651-0035
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Applicati n Number	09/655,596
Filing Date	09-06-00
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Name	R.L. Smi	ith McKeithen, Se	cretary, Cadence De	sign Systems	,Inc.			
Signature	DAN	4. Keik	•					
Date								
NOTE: Signa	atures of a	Ill the inventors of	or assignees of reco	rd of the enti	re interest or t	heir repr	resentative(s) are required.	
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WHEREAS, IBM has entered into a "Patent Assignment Agreement" (the "Agreement") with CADENCE DESIGN SYSTEMS, INC., a Delaware corporation ("CADENCE"), dated February 27, 2002 ("Effective Date"), pursuant to which IBM has agreed to, among other things, assign to CADENCE certain United States patents.

WHEREAS, IBM and CADENCE wish to evidence the assignment of such patent rights made in the Agreement with this document.

NOW, THEREFORE, for good and valuable consideration, IBM agrees as follows:

ARTICLE I IBM PATENTS

RECEIVED

MAY 0 9 2002

"IBM Patents" shall mean the patent applications listed in Attachment A.

Technology Center 2100

ARTICLE II ASSIGNMENTS

Effective on the Effective Date, and subject to the terms and conditions of the Agreement, including certain reserved rights, IBM has assigned, transferred and conveyed to CADENCE all of IBM's right, title and interest in the IBM Patents.

IN WITNESS WHEREOF, IBM has caused this Notice to be executed.

INTERNATIONAL BUSINESS MACHINES CORPORATION

Gerald Rosenthal

Vice President, Intellectual Property and Licensing

Date: March 8, 2002

ATTACHMENT A TO IBM PATENT ASSIGNMENT ACKNOWLEDGEMENT

09/655,596

09/656,541

09/193,733

09/523,053

09/656,146

09/373,125

09/655,595

09/656,147

09/796,055

09/522,354

ASSIGNMENT OF PATENT APPLICATIONS

Whereas, CADENCE DESIGN SYSTEMS, INC., a corporation organized and existing under and by virtue of the laws of the Delaware and having its place of business at 2655 Seely Avenue, San Jose, California 94134 ("CADENCE"), owns of all right, title and interest in the inventions claimed and/or disclosed in following United States Patent Applications ("the Patent Applications"):

Patent Application Number	Date Patent Application Filed	Inventors	Title
09/655,596	09-06-00	Beausoleil, W.F., et al	High Speed Software Driven Emulator Comprised of a Plurality of Emulation Processors with a Method to Allow Memory Read/Writes Without Interrupting the Emulation
09/656,541	09-06-00	Beausoleil, W.F., et al	High Speed Software Driven Emulator Comprised of a Plurality of Emulation Processors with a Method to High Speed Bulk Read/Write Operation Synchronous DRAM While Refreshing the Memory
09/193,733	11-17-98	Ruedinger, J.	Algorithm to Separate Clock Domains for Emulation Purposes
09/523,053	03-10-00	Ruedinger, J., et al	Methods to Improve Concurrent Behavior Modeling with Emulation
09/656,146	09-06-00	Beausoleil, W.F., et al	High Speed Software Driven Emulator Comprised of a Plurality of Emulation Processors with

			Improved Multiplexed Data Memory
09/373,125	08-12-99	Beausoleil, W.F., et al	Clustered Processors in an Emulation Engine
09/655,595	09/06/00	Beausoleil, W.F., et al	High Speed Software Driven Emulator Comprised of a Plurality of Emulation Processors with an Improved Board-to- Board Interconnection Cable
09/656,147	09/06/00	Beausoleil, W.F., et al	High Speed Software Driven Emulator Comprised of a Plurality of Emulation Processors with an Improved Maintenance Bus that Streams Data
09/796,055	02-28-00	Beausoleil, W.F., et al	High Bandwidth 3D Memory Packaging Technique
09/522,354	03-09-00	Musselman, R., et al	Non-synchronous Hardware Emulator

Whereas QUICKTURN DESIGN SYSTEMS, INC., a corporation organized and existing under and by virtue of the laws of the Delaware and having its place of business at 2655 Seely Avenue, San Jose, California 94134 ("QUICKTURN"), is desirous of acquiring the entire right, title and interest in the inventions claimed and/or disclosed in the Patent Applications;

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, CADENCE hereby sells, assigns, transfers and sets over unto QUICKTURN, its successors and assigns, the full and exclusive right, title and interest to the inventions claimed and/or disclosed in the Patent Applications and to all Letters Patent or application or similar legal protection, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for the inventions claimed and/or disclosed in the Patent Applications, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under the International Convention; the Patents to be held and enjoyed by QUICKTURN for its own use and behalf, and for QUICKTURN'S legal

representatives and assigns, to the full end of the term for which any Letters Patents are granted, as fully and entirely as the same would have been held by CADENCE had this assignment and sale not been made. CADENCE hereby authorizes and requests the United States Commissioner of Patents and Trademarks and any officials of foreign countries whose duty it is to issue patents or any legal equivalent thereof to QUICKTURN, its successors and assigns, in accordance with this Assignment.

Executed this 22 Nd day of day of	April	, 2002, at
7	Men. Ken.	
	R.L. Smith McKeith Secretary, CADENC	nen CE DESIGN SYSTEMS, INC.
STATE OF CALIFORNIA) ss		
COLDENIA OF CANDA OF ADA	M. Brock	nersonally appeared
On 4/22/0 before me, Joyce R.L.Sm: Hh Mcko. Then personal of satisfactory evidence to be the person(e) instrument and acknowledged to me that he authorized capacity(ies), and that by his/he the entity upon behalf of which the person(e)	whose name(s) is/a re : c/s he/the y executed the c/the ir signature (s) on t	subscribed to the within e same in his/her/their the instrument the person(s), or
JOYCE M. BROCK	Joyce o	M. Brock I for said County and State